09/09/2023

NOTICE - GENERAL

Internal Smart India Hackathon 2023

Institution's Innovation Council (IIC), SRMCEM is pleased to inform all that Ministry of Education's Innovation Cell (MIC) in coordination with All India Council for Technical Education (AICTE) has launched Smart India Hackathon (SIH) 2023. In the last 6 years, SIH has evolved into the world's Largest Open Innovation Model to solve some of the pressing problems we face in our daily lives, and thus inculcate a culture of product innovation and problem-solving mindset.

This year's edition also focuses on software and hardware approaches to develop innovative solutions against the challenges faced by India's governing agencies including Ministries, Departments and PSUs as well as various private organizations. Total number of problem statements published on the SIH portal are as follows:

- Hardware: 58
- Software: 181

To contribute in the nation building process we are organizing **"Internal Smart India Hackathon 2023"** at institute level from 14th September-23rd September 2023. All the faculty members are advised to visit on **www.sih.gov.in** and encourage students to visit the same to get familiarise and select problem statements for further solutions. Important stages and requirements for SIH2023 participation and nomination of teams for national level selection process are elaborated in Annexture-1.

All are invited to celebrate and contribute in this journey of innovation with enthusiasm and creativity.

Er. Ashish Tiwari
President (IIC)

14/09/23
Prof. (Dr.) Bhavesh Kr. Chauhan
Director

Stages and Deadlines of SIH2023:

1. SPOC Registration (Completed). Er. Ashish Tiwari, President (IIC) is the SPOC for SIH2023.
2. SPOC verification and approval by MoE's Innovation Cell.
3. Conduction of Internal Smart India Hackathon (at institute level, initiated with this notification), to select teams for nomination at national level (see the flow diagram of Internal hackathon conduction).
4. Team formation guideline:
    i. All team members should be from same institute; no inter-institute teams are allowed. However, members from different branches of the same institute are encouraged to form a team.
    ii. Each team would mandatorily comprise of 6 members including the team leader.
    iii. Each team must have AT LEAST ONE FEMALE TEAM MEMBER.
    iv. As the software edition of the hackathon is digital product development competition, majority of the team members MUST be well versed with programming skills. For the hardware edition, we encourage multi-disciplinary teams – which means your team should have a good mix of Mechanical Engineers, Electronic Engineers, Civil Engineers, Product Designers and Programmers, etc.
    v. The nominated teams should have participated in the recommended Internal hackathon.
5. Maximum two mentors per team are allowed. It is strongly recommended to have at least one mentor from industry side.
6. Total number of teams (maximum) to be nominated from the institute are 35 (30 shortlisted+5 waitlisted).
7. Team registration and solution submission portal is open now.
8. Team leader of the nominated teams must register on the portal (21st-23rd September) and must submit their solution.
9. SPOC needs to approve these submissions through the SPOC portal (mandatory step).

Internal Hackathon Conduction

SPOC (SIH2023)

- To communicate on the behalf of Institute.
- To conduct and coordinate Internal Hackathon.
- To approve the teams and their submissions.

Core Group of Hardware Track

[HoDs of (EC,EE & EN, ME, CE)] and one nominated senior faculty member from software side (Prof. Gaurav Mishra).

(HoD ME will be the coordinator of this core group)

This core group will be responsible to:

- Shortlist at least 20 hardware problems which will be given to student teams for solution.
- Formation and facilitation for inter disciplinary student teams.
- Allotment of mentors.
- Organize hackathon before final shortlisting of teams.
- Presentation and evaluation of solution.

Core Group of Software Track

[HoDs of (CSE, IT,BCA), Prof. Gaurav Mishra] and one nominated senior faculty member from hardware side (HoD, ME).

(Prof. Gaurav Mishra will be the coordinator of this core group)

This core group will be responsible to:

- Shortlist at least 20 software problems which will be given to student teams for solution.
- Formation and facilitation for inter disciplinary student teams.
- Allotment of mentors.
- Organize hackathon before final shortlisting of teams.
- Presentation and evaluation of solution.